

	Type	L #	Hits	Search Text	DBs	Time Stamp
1	BRS	L1	3949	(controller NEAR3 motor) WITH (CPU OR processor OR microprocessor OR microcomputer OR (micro ADJ processor) OR (micro ADJ computer))	USPAT; US-PGP UB; EPO; JPO; DERWE NT; IBM_T DB	2003/12/13 18:24
2	BRS	L2	24191	378/\$.ccls.	USPAT; US-PGP UB; EPO; JPO; DERWE NT; IBM_T DB	2003/12/13 18:17
3	BRS	L3	29	1 AND 2	USPAT; US-PGP UB; EPO; JPO; DERWE NT; IBM_T DB	2003/12/13 18:17
4	BRS	L4	15596	((application ADJ specific ADJ integrated ADJ circuit) OR ASIC) WITH (CPU OR processor OR microprocessor)	USPAT; US-PGP UB; EPO; JPO; DERWE NT; IBM_T DB	2003/12/13 18:21
5	BRS	L5	2131	(motor ADJ controller) WITH (CPU OR processor OR microprocessor OR microcomputer OR (micro ADJ processor) OR (micro ADJ computer))	USPAT; US-PGP UB; EPO; JPO; DERWE NT; IBM_T DB	2003/12/13 18:25

	Type	L #	Hits	Search Text	DBs	Time Stamp
6	BRS	L6	1	(dedicated ADJ motor ADJ controller) WITH (CPU OR processor OR microprocessor OR microcomputer OR (micro ADJ processor) OR (micro ADJ computer))	USPAT; US-PGP UB; EPO; JPO; DERWE NT; IBM_T DB	2003/12/13 18:30
7	BRS	L7	0	(dedicated NEAR3 (motor ADJ controller)) WITH (CPU OR processor OR microprocessor OR microcomputer OR (micro ADJ processor) OR (micro ADJ computer)) WITH (reduc\$3 OR lessen OR lighten) WITH (load OR process)	USPAT; US-PGP UB; EPO; JPO; DERWE NT; IBM_T DB	2003/12/13 18:31